

## **DETAILED ACTION**

### ***Response to Amendment***

This Office Action has been issued in response to amendment filed 25 September 2009. Applicant's arguments have been carefully and fully considered and are persuasive.

### ***Claim Status***

Claims 1-5, 7-8, 18-22, 24-26, 56-60, 62-64, 75-79, 81-83, 94-98, 100-101, and 111-116 remain pending and are ready for examination.

### ***Allowable Subject Matter***

Claims **1-5, 7-8, 18-22, 24-26, 56-60, 62-64, 75-79, 81-83, 94-98, 100-101, and 111-116** are allowed.

The following is an Examiner's statement of reasons for allowance: the prior art made of record fails to teach the combination including the limitation of:

(Claim **1**) "...monitoring the values of each of the data elements in the allocation register; and in response to each of said data elements having the value corresponding to an allocated buffer, writing the data elements in the allocation register back to the allocation memory; identifying a set including at least one data element having the value corresponding to an available buffer; and writing the set to the allocation register.";

(Claim **18**) "...monitor the values of each of the data elements in the allocation register; and in response to each of the data elements having the value corresponding to an allocated buffer: write the data elements in the allocation register back to the

allocation memory, identify a set including at least one data element having the value corresponding to an available buffer, and write the set to the allocation register.”;

(Claim 56) “...monitor the values of each of the data elements in the allocation register; and in response to each of the data elements having the value corresponding to an allocated buffer: write the data elements in the allocation register back to the allocation memory, identify a set including at least one data element having the value corresponding to an available buffer, and write the set to the allocation register.”;

(Claim 75) “...means for monitoring the values of each of the data elements in the allocation register; means for, in response to each of said data elements having the value corresponding to an allocated buffer: writing the data elements in the allocation register back to the allocation memory, identifying a set including at least one data element having the value corresponding to an available buffer, and writing the set to the allocation register.”;

(Claim 94) “...monitoring the values of each of the data elements in the allocation register; and in response to each of the data elements having the value corresponding to an allocated buffer: writing the data elements in the allocation register back to the allocation memory, identifying a set including at least one data element having the value corresponding to an available buffer, and writing the set to the allocation register.”;

(Claim 112) “...identifying a data element associated with the buffer in one of the allocation memory and the allocation register, and changing a value of the data element to a value corresponding to an available buffer, in response to identifying the data element associated with said buffer in the allocation memory: writing the set including

said data element to a clear register, and after changing the value of said data element in the clear register, writing the set in the clear register to the allocation memory.”;

(Claim 113) “...identify a data element associated with the buffer in one of the allocation memory and the allocation register, and change a value of the data element to a value corresponding to an available buffer; and in response to identifying the data element associated with the buffer in the allocation memory: write the set including the data element to a clear register, and after changing the value of the data element in the clear register, write the set in the clear register to the allocation memory.”;

(Claim 114) “...identify a data element associated with the buffer in one of the allocation memory and the allocation register, and change a value of the data element to a value corresponding to an available buffer, and in response to identifying the data element associated with the buffer in the allocation memory: write the set including the data element to a clear register, and after changing the value of the data element in the clear register, write the set in the clear register to the allocation memory.”;

(Claim 115) “...identifying a data element associated with the buffer in one of the allocation memory and the allocation register; and changing a value of the data element to a value corresponding to an available buffer; means for writing the set including the data element to a clear register in response to identifying the data element associated with the buffer in the allocation memory; and means for writing the set in the clear register to the allocation memory after changing the value of the data element in the clear register.”;

(Claim 116) "...identifying a data element associated with the buffer in one of the allocation memory and the allocation register, and changing a value of the data element to a value corresponding to the available buffer; and in response to identifying the data element associated with the buffer in the allocation memory: writing the set including the data element to a clear register, and after changing the value of the data element in the clear register, writing the set in the clear register to the allocation memory. ";

As dependent claims 2-5, 7-8, 19-22, 24-26, 57-60, 62-64, 76-79, 81-83, 95-98, 100-101, and 111 depend from an allowable base claim; they are at least allowable for the same reasons as noted *supra*.

The prior art made of record, Chen et al (U.S. 2003/0093629) Shemla et al (U.S. RE38,821), neither anticipates nor renders obvious the above-recited combinations for at least the reasons specified and as shown in Applicant's Arguments filed 6 January 2009.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew Bradley whose telephone number is (571) 272-8575. The examiner can normally be reached on 6:30-3:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Christian Chace can be reached on (571) 272-4190. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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